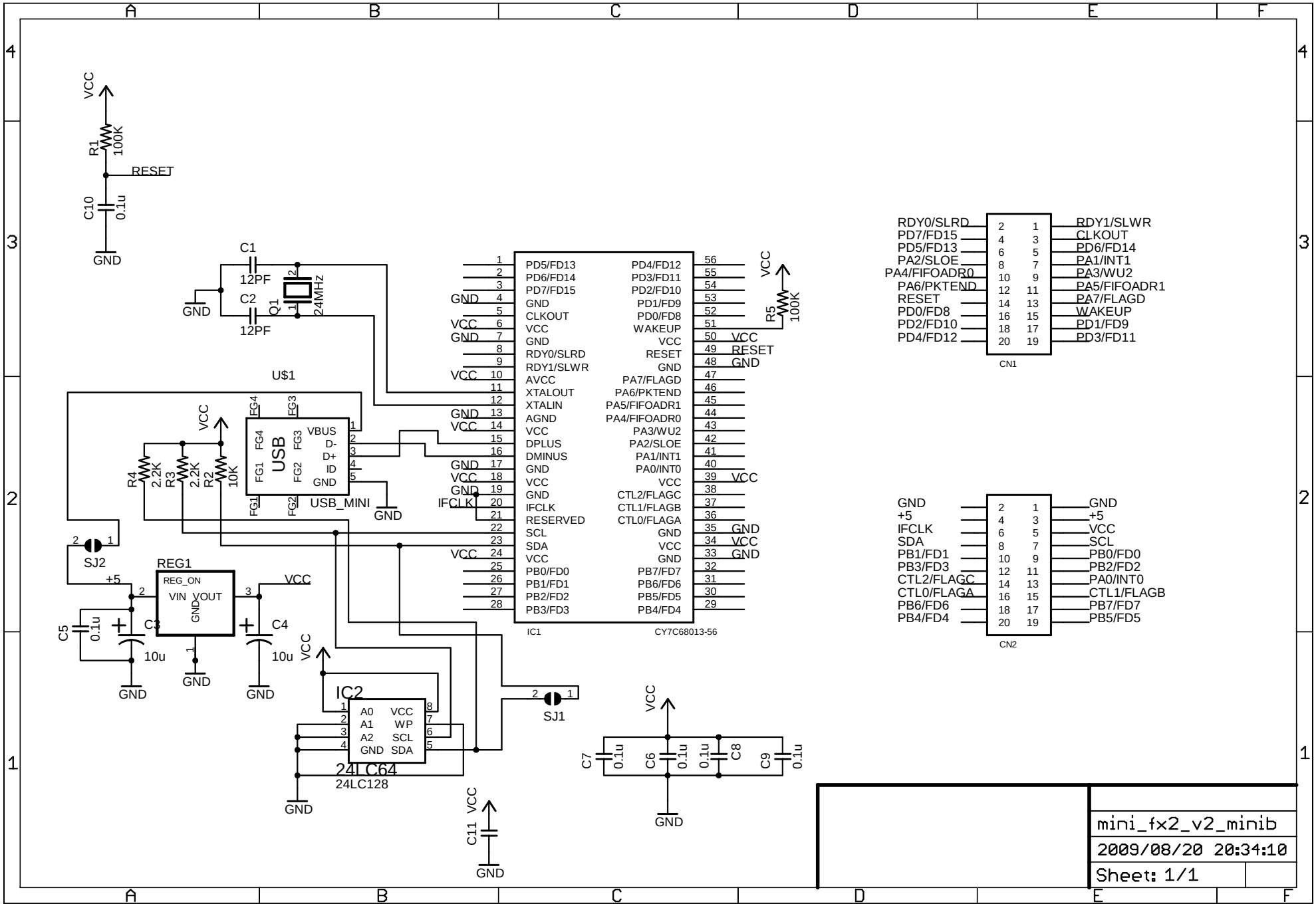
VCC
R1
100K
RESET
C10
0.1u
GND
C1
12PF
C2
12PF
Q1
24MHz
U$1
USB
VBUS
D-
D+
ID
GND
USB_MINI
R4
2.2K
R3
2.2K
R2
10K
SJ2
REG1
REG_ON
VIN
VOUT
C5
0.1u
C3
10u
C4
10u
IC2
A0
A1
A2
GND
VCC
WP
SCL
SDA
24LC64
24LC128
SJ1
C11
IC1
CY7C68013-56
PD5/FD13
PD6/FD14
PD7/FD15
CLKOUT
RDY0/SLRD
RDY1/SLWR
AVCC
XTALOUT
XTALIN
AGND
DPLUS
DMINUS
IFCLK
RESERVED
SCL
SDA
PB0/FD0
PB1/FD1
PB2/FD2
PB3/FD3
PD4/FD12
PD3/FD11
PD2/FD10
PD1/FD9
PD0/FD8
WAKEUP
RESET
PA7/FLAGD
PA6/PKTEND
PA5/FIFOADR1
PA4/FIFOADR0
PA3/WU2
PA2/SLOE
PA1/INT1
PA0/INT0
CTL2/FLAGC
CTL1/FLAGB
CTL0/FLAGA
PB7/FD7
PB6/FD6
PB5/FD5
PB4/FD4
R5
100K
CN1
RDY0/SLRD
PD7/FD15
PD5/FD13
PA2/SLOE
PA4/FIFOADR0
PA6/PKTEND
RESET
PD0/FD8
PD2/FD10
PD4/FD12
RDY1/SLWR
CLKOUT
PD6/FD14
PA1/INT1
PA3/WU2
PA5/FIFOADR1
PA7/FLAGD
WAKEUP
PD1/FD9
PD3/FD11
CN2
GND
+5
IFCLK
SDA
PB1/FD1
PB3/FD3
CTL2/FLAGC
CTL0/FLAGA
PB6/FD6
PB4/FD4
VCC
SCL
PB0/FD0
PB2/FD2
PA0/INT0
CTL1/FLAGB
PB7/FD7
PB5/FD5
C7
0.1u
C6
0.1u
C8
0.1u
C9
0.1u
mini_fx2_v2_minib
2009/08/20 20:34:10
Sheet: 1/1